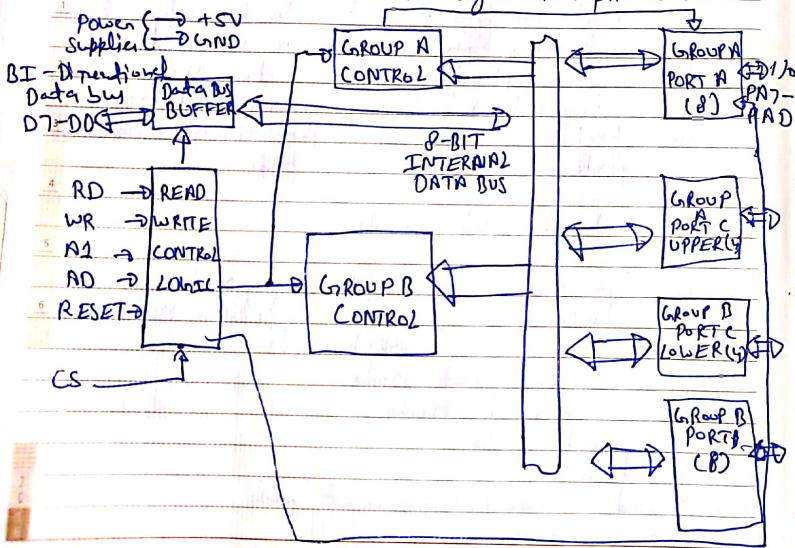


S	M	T	W	T	F	S
	1	2	3	4	5	
6	7	8	9	10	11	12
13	14	15	16	17	18	19
20	21	22	23	24	25	26
27	28	29	30	31		

Section - 5

Q2 Draw the internal architecture of 8255 microprocessor. Discuss the interrupt control, serial input output control in 8085 microprocessor.

ANSWER PPI 8255 is a general purpose programmable I/O device designed to interface the CPU with its outside world such as ADC, DAC, keyboard etc. We can program it according to the given condition. It can be used with almost any microprocessor.



M	T	W	T	F	S	S
30	31					1
2	3	4	5	6	7	8
9	10	11	12	13	14	15
16	17	18	19	20	21	22
23	24	25	26	27	28	29

Interrupt Control in 8085 microprocessor:-

Interrupt in 8085: Interrupts are the signals generated by the external devices to request the microprocessor to perform a task. There are 5 interrupt signals i.e. TRAP, RST 7.5, RST 6.5, RST 5.5 and INTR.

Serial input output control in 8085 microprocessor:-

8085 Microprocessor has two serial input/output pins that are used to read/write one bit data to and from peripheral devices. There is a one bit output port inside the 8085 CPU (pin number 4) 1 bit data can be externally written in this port. - To write data into this port, SIM instruction is used.