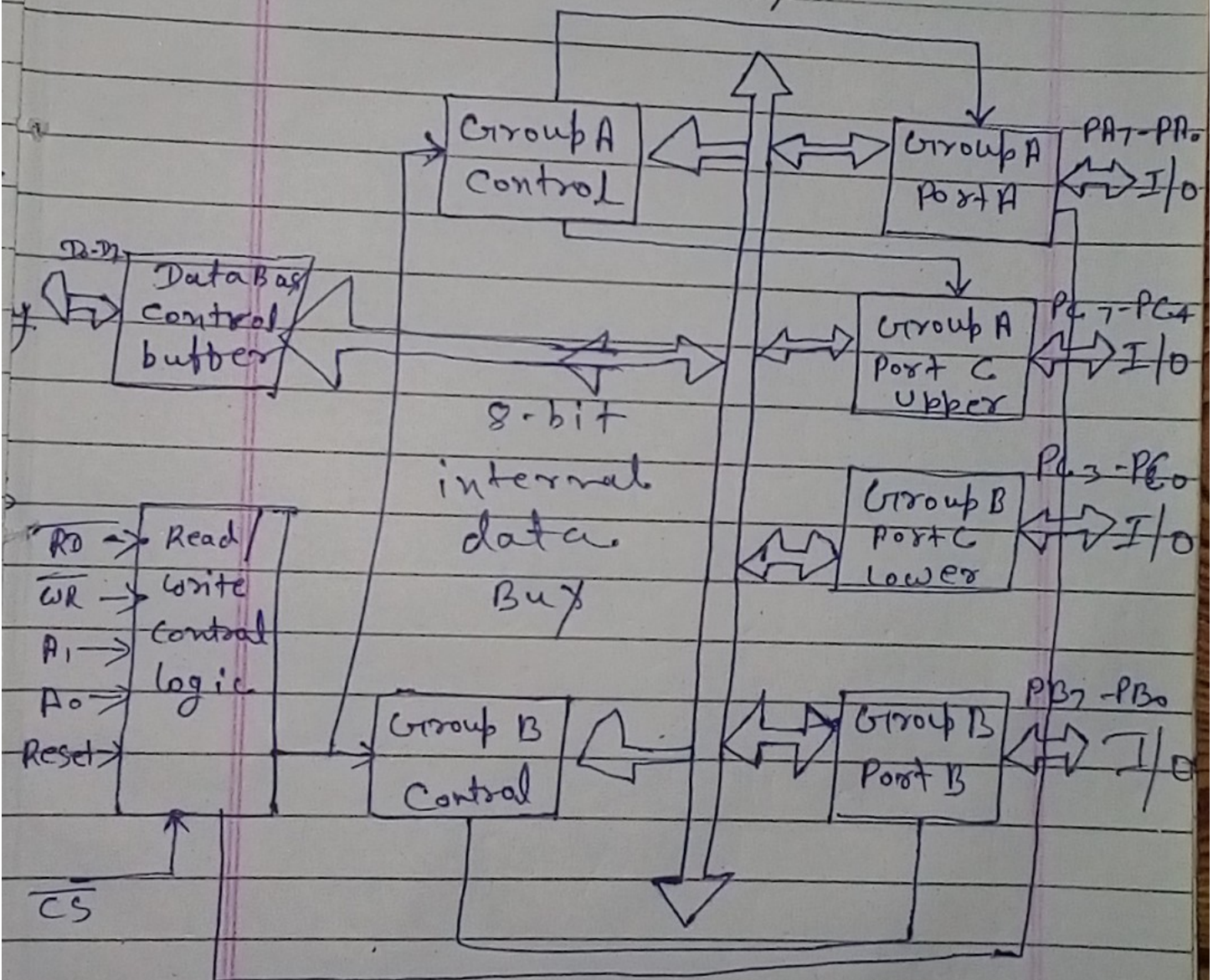


# SEC-5

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② Draw the internal architecture of 8255 P.P. Discuss the interrupt control, serial input output control in 8085 microprocessor.





## Interrupt Control. $\rightarrow$

a) The 8085 have 5 hardware interrupt signals: RST 5.5, RST 6.5, RST 7.5, TRAP, and INTR.

b) The microprocessor recognize interrupt request on these lines at the end of the current instruction execution.

c) The  $\overline{INTA}$  signal is used to accept that the processor has acknowledge in INTR interrupt.

## Serial Input output control $\rightarrow$

a) SID  $\rightarrow$  This input signal is used to accept serial data bit from the external device.

b) SOD  $\rightarrow$  This is an output signal which enable the transmission of serial data bit by bit to external device.