

## SEC-B

② Draw the busward. architecture of microprocessor. Draw the internal architecture of 8085 microprocessor.

Harward. architecture :-

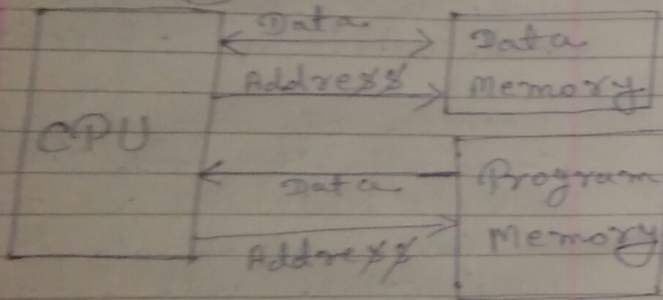
a) Harward. architecture use sepearate. memories for program and data with their independent, address and data. lines/buses.

b) Because of two different streams of data and address, there is no need to have any time division multiplexing and data buses.

c) Not only, the architecture supports parallel buses for addressing, mode and data but also allow different internal organization such that instruction can be prefected and decoded while while multiple.

data and being fetched and operated on.

④ Further, the data bus may have different size than the address bus. This allow the optimal bus width of the data and addressing mode.



internal architecture of 8085

