Single Phase Half-Wave Reetifiers. Cols= Vm sinwt It consists of a SCR, AC voltage source (Vs) & load. The load may be purely resistive, indutive or RL load. A thyristor blocks the flow of load current (10) until it is triggered. At some delay angle (x), at the gate signal applied blu gate & cathode turns on the BCR. Immediately till supply voltage is applied to load as Vo. At the instant of delay angle (a), vo rises upto Vs 1-e., Umisinat for resistive load to is in phase with Vo. But as soon as the Vs becomes zero at cot = 17, 311 etc., the load current will become zero 4 after cot = 17, SCR is reversed blased & T will turn off at wit = 17. Thyristort coll remain in off condition till it is froed again at wt = (217+0x) after delay (2) I hyristor romains ON from Cot = dtotto (211+x) to 311 etc., during these intervals 4=0. If is Off from 17 to (217+4), 317 to (417+4) etc. during these entoruly VT has the waveshapes





