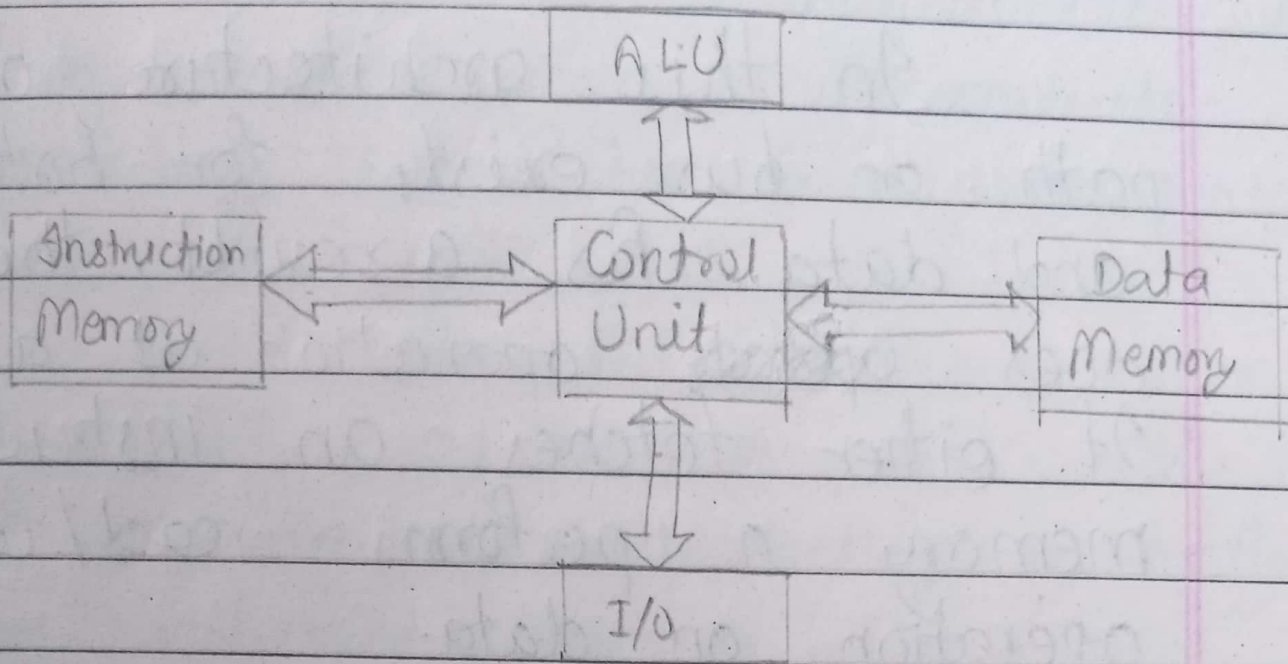


# Harvard Architecture of Microprocessor -



The Harvard architecture is a computer architecture with separate storage & signal pathways for instructions & data.

This architecture has data storage entirely contained within the CPU; There is no access to the instruction storage as data. Programs needed to be loaded by an operator, the processor could not boot itself. There is no need to make two memories share properties.

