



# DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

## STATEMENT OF MARKS

### Master of Technology - FIRST YEAR

VLSI Design

Session.: 2013-14

Name of Candidate

: JITENDRA KUMAR

Father's Name

: SURESH KUMAR SINGH

Name of Institution

: SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY, ALLAHABAD

Sl. No.: 17981527

Roll No.: 1316208512

Enrollment No : 13162085254

### SECOND SEMESTER

Subject Code and Name	Maximum Marks :		Obtained Marks :	
	Sess. Exam.	Total	Sess. Exam.	Total
VD201 Analog VLSI Design	050	100	046	045
VD202 Low Power VLSI Design	050	100	047	043
VD203 Algorithm for VLSI Design Automation	050	100	046	042
VD024 Designing With ASICs	050	100	046	043
PRACTICAL				
TOTAL				
	200	400	185	173
358				

Total No.	Carry Over (if any) Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
			SECOND SEMESTER	Max. Marks	
			358	600	PASS

Checked by

Printed on : 14/07/17

Controller of Examination

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of U.P. on 18th May, 2000 vide Act No. 1248(2)XXVI-V-1-1983-19-2003 On 18th February, 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gaurigan Budh Technical University, Lucknow and a new University was created namely Mahatma Technical University, Meera, vide Notification No. 238(2)XXIX-V-1-10-1948-2010, On 31st October, 2013 Gaurigan Budh Technical University, Lucknow and Mahatma Technical University, Meera were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324(15-1-2015-1)2009, Government of U.P. vide Notification No. 1158(2)XXIX-V-1-15-1942a-2015 dated 9th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

