



# DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

## STATEMENT OF MARKS Master of Technology - FIRST YEAR

VLSI Design

Session.: 2015-16

Name of Candidate : MEHREEN  
Father's Name : MOHD. ANWAR  
Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY, ALLAHABAD

SECOND SEMESTER

Sl. No.: 17961289  
Roll No.: 1516208511  
Enrollment No : 151620808489

Subject Code and Name	Maximum Marks :			Obtained Marks :		
	Sess.	Exam.	Total	Sess.	Exam.	Total
VD201 Analog VLSI Design	050	100	150	049	068	117
VD202 Low Power VLSI Design	050	100	150	047	060	107
VD203 Algorithm for VLSI Design Automation	050	100	150	047	074	121
VD024 Designing With ASICs	050	100	150	048	073	121
PRACTICAL						
TOTAL						
	200	400	600	191	275	466

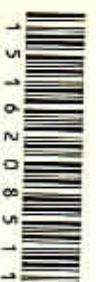
Total No.	Carry Over (If any) Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
			SECOND SEMESTER 466	Max. Marks 600	
					PASS

Checked by:

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2006 vide Act No. 13492XVI/2005-1(hn)-118-2005 On 10th February 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gaurav Boddh Technical University, Lucknow and a new University was created namely Muzaffarpur Technical University, Noida vide Notification No. 238(2)/XXIX/05-1-10-1164/623/10 On 31st October 2013 Gaurav Boddh Technical University, Lucknow and Muzaffarpur Technical University, Lucknow were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 5324/15-12016-1137/2008, Government of UP, vide Notification No. 1158(2)/XXIX/05-1-15-1164/24-5015 dated 9th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

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Controller of Examination



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