



DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

STATEMENT OF MARKS

Master of Technology - FIRST YEAR

VLSI Design

Session : 2015-16

Name of Candidate : ROHIT YADAV
Father's Name : RAMOO LAL YADAV

Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING & TECHNOLOGY, ALLAHABAD

Sl No.: 17961495
Roll No.: 1516208519
Enrollment No : 151620808497

SECOND SEMESTER

Subject Code and Name	Maximum Marks :		Obtained Marks :	
	Sess.	Exam. Total	Sess.	Exam. Total
VD201 Analog VLSI Design	050	100	044	038
VD202 Low Power VLSI Design	050	100	043	057
VD203 Algorithm for VLSI Design Automation	050	100	043	053
VD204 Designing With ASICS	050	100	044	050
PRACTICAL				
TOTAL				
	200	400	600	174
				198
				372

Total No.	Grace Sub Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks	Max. Marks	RESULT
1	VD201(2),	2	SECOND SEMESTER 372	600	PASS

Checked by :

Printed on : 13/07/17

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 1248(2)XXVII-V-1-1(ka)-19-2000 On 16th February, 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gautam Buddha Technical University, Lucknow and a new University was created namely Mahatma Technical University, Noida, vide Notification No. 236(2)XXX-V-1-10-1(ka)-6-2010. On 31st October, 2013 Gautam Buddha Technical University, Lucknow and Mahatma Technical University, Noida were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324(16-1-2016-1(3)/2009. Government of UP, vide Notification No. 1156(2)LXXX-V-1-15-1(ka)24-2015 dated 9th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

Controller of Examination