



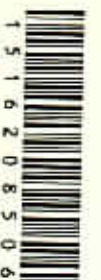
# DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

## STATEMENT OF MARKS

### Master of Technology - FIRST YEAR

#### VLSI Design



Session.: 2015-16

Name of Candidate : CHAHAT VARSHNEY  
Father's Name : RUPAK VARSHNEY  
Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY, ALLAHABAD

SECOND SEMESTER

Sl. No.: 17961283  
Roll No.: 1516208506  
Enrollment No.: 151620808484

Subject Code and Name	Maximum Marks :		Obtained Marks :	
	Sess.	Exam, Total	Sess.	Exam, Total
VD201 Analog VLSI Design	050	100	049	047
VD202 Low Power VLSI Design	050	100	049	075
VD203 Algorithm for VLSI Design Automation	050	100	048	075
VD024 Designing With ASICs	050	100	049	073
PRACTICAL				
TOTAL				
	200	400	195	270
				465

Total No.	Carry Over (if any) Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
			SECOND SEMESTER	Max. Marks	
			465	600	PASS

Checked by:

Printed on : 16/06/17

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 9th May, 2000 vide Act No. 1348/2001-4-1 (10th) 18-2000 On 10th February, 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gauri Baidh Technical University, Lucknow and Mahendra Technical University, Lucknow were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324/16-1-2015-13/2009, Government of UP, vide Notification No. 15802/2009-4-1-15-10/26-2015 dated 8th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow.

Controller of Examination