

Father's Name

: VINOD KUMAR YADAV : SURYABALI YADAV : SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY , ALLAHABAD

SECOND SEMESTER

Name of Institution Name of Candidate

VD024 VD203 VD202 VD201

DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

Master of Technology - FIRST YEAR STATEMENT OF MARKS

VLSI Design

Session.: 2014-15

SI. No.: 17961509 Roll No.: 1416208509 Enrollment No : 141620816211

Designing With ASICS Algorithm for VLSI Design Automation Low Power VLSI Design Analog VLSI Design Subject Code and Name PRACTICAL TOTAL 200 050 Sess. Exam. 050 050 Maximum Marks: 400 100 100 100 600 Total 150 150 150 150 Sess. Exam. 183 046 046 045 046 Obtained Marks: 192 048 051 046 047 375 Total 092 096 094 093

	Total No.	C
	Subject(s) Code	Cally Over (II ally)
	(if anv)	Grace Marks
375	SECOND SEMESTED MON A	Marke Ohtaina
600	Max Marks	d / Maximum Maria
PASS	RESULT	

Checked by : Lend

Printed on: 13/07/17

Note: The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 1248(2)XIX-V-1-1(8)-1-2000 On 16th February, 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gautam Budch Technical University, Lucknow was renamed as Gautam Budch Technical University, Pradesh Technical University, Lucknow was renamed as Gautam Budch Technical University, University, Lucknow and Mahamaya Technical University, Notida were merged to form Uttar Pradesh, Lucknow and Mahamaya Technical University, Notida were merged to form Uttar University, Uttar Pradesh, Lucknow and Mahamaya Technical University, Notida were merged to form Uttar University, Uttar Pradesh, Lucknow and Mahamaya Technical University, Notida were merged to form Uttar University, Uttar Pradesh, Lucknow as Dr. A.P.J.Abdul Kalam Technical University, Uttar Pradesh, Lucknow Controller of Examination