



DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

STATEMENT OF MARKS

Master of Technology - FIRST YEAR

VLSI Design

Session: 2013-14

Name of Candidate

: ASHISH KUMAR

Father's Name

: BHIMBALI PRAJAPATI

Name of Institution

: SHAMBHUNATH INSTITUTE OF ENGINEERING & TECHNOLOGY, ALLAHABAD

Sl. No.: 17961513
Roll No.: 1316208504
Enrollment No.: 131620858246

SECOND SEMESTER

Subject Code and Name	Maximum Marks :			Obtained Marks :		
	Sess.	Exam.	Total	Sess.	Exam.	Total
VD201 Analog VLSI Design	050	100	150	047	056	103
VD202 Low Power VLSI Design	050	100	150	046	061	107
VD203 Algorithm for VLSI Design Automation	050	100	150	047	053	100
VD024 Designing With ASICs	050	100	150	047	057	104
PRACTICAL						
TOTAL						
	200	400	600	187	227	414

Total No.	Carry Over (if any)	Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
				SECOND SEMESTER	Max. Marks	
				414	600	PASS

Checked by :

Printed on : 13/07/17

Controller of Examination

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 1248(2)XVII-V-1-(ka)-19-2000 On 16th February, 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gaudam Buddh Technical University, Lucknow and a new University was created namely Mahanaya Technical University, Noida, vide Notification No. 236(2)JLXXX-V-1-10-1(ka)-6-2010. On 31st October, 2013 Gaudam Buddh Technical University, Lucknow and Mahanaya Technical University, Lucknow were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324/16-1-2016-1(3)2009, Government of UP, vide Notification No. 1156(2)JLXXX-V-1-15-1(ka)24-2015 dated 9th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

