



DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

STATEMENT OF MARKS

Master of Technology - FIRST YEAR

VLSI Design



Session: 2015-16

Name of Candidate : GUNJAN AGARWAL
Father's Name : RAVINDRA AGARWAL
Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY, ALLAHABAD

Subject Code and Name : SECOND SEMESTER

SI. No.: 17961284
Roll No.: 1516208507
Enrollment No.: 151620808485

Subject Code and Name	Maximum Marks :		Obtained Marks :	
	Sess. Exam.	Total	Sess. Exam.	Total
VD201 Analog VLSI Design	050	100	047	054
VD202 Low Power VLSI Design	050	100	045	064
VD203 Algorithm for VLSI Design Automation	050	100	046	057
VD024 Designing With ASICs	050	100	047	066
PRACTICAL				
TOTAL				
	200	400	185	241
		600		426

Total No.	Carry Over (if any)	Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
				SECOND SEMESTER	Max. Marks	
				426	600	PASS

Checked by:

Printed on : 16/06/17

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 32&33/XXI/4-1-1999-19-2000 On 16th February 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gaurish Buddh Technical University, Lucknow and a new University was created namely Mahatma Jyoti Bapu Technical University, Noida, vide Notification No. 23&24/XXI/4-1-10-14/31-2-2010, On 31st October, 2013 Gaurish Buddh Technical University, Lucknow and Mahatma Jyoti Bapu Technical University, Noida were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 33&34/16-1-2013-13-2009, Government of UP, vide Notification No. 11&12/XXI/4-1-15-10/31-2-2015 dated 31st September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

Controller of Examination