



DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

STATEMENT OF MARKS

Master of Technology - FIRST YEAR

VLSI Design



1 5 1 6 2 0 8 5 0 9

Session: 2015-16

Name of Candidate : KM. NASREEN
Father's Name : MOHAMMAD ANWAR
Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING & TECHNOLOGY, ALLAHABAD

Sl. No.: 17961287
Roll No.: 1516208509
Enrollment No.: 151620808487

SECOND SEMESTER

Subject Code and Name	Maximum Marks :		Obtained Marks :	
	Sess. Exam.	Total	Sess. Exam.	Total
VD201 Analog VLSI Design	050	100	049	073
VD202 Low Power VLSI Design	050	100	048	070
VD203 Algorithm for VLSI Design Automation	050	100	047	072
VD024 Designing With ASICs	050	100	048	066
TOTAL	200	400	192	281
				473

Total No.	Carry Over (if any) Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
			SECOND SEMESTER	Max. Marks	
			473	600	PASS

Checked by

Printed on : 16/06/17

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 12492/VV/JU-1-1049-16-2000 On 18th February 2016, Uttar Pradesh Technical University, Lucknow was retained as Gautam Buddha Technical University, Lucknow and a new University was created namely Mahatma Jyotiba Phule Technical University, Noida, vide Notification No. 2362/LX/003-A-1-10-16-24-2016, On 31st October 2013 Gautam Buddha Technical University, Lucknow and Mahatma Jyotiba Phule Technical University, Noida were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324/16-1-2016-10/2008, Government of UP vide Notification No. 119620/003-A-1-15-16-24-2015 dated 8th September, 2015 retained Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

Controller of Examination