



# DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

## STATEMENT OF MARKS

Master of Technology - FIRST YEAR

VLSI Design



1 5 1 6 2 0 8 5 2 0

Session.: 2015-16

Name of Candidate : SATYENDRA KUMAR

Father's Name : RAJBALI RAM

Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING & TECHNOLOGY, ALLAHABAD

Sl. No.: 17981496

Roll No.: 1516208520

Enrollment No : 151620808498

### SECOND SEMESTER

Subject Code and Name	Maximum Marks :			Obtained Marks :		
	Sess. Exam.	Total		Sess. Exam.	Total	
VD201 Analog VLSI Design	050	100	150	048	036	084
VD202 Low Power VLSI Design	050	100	150	047	054	101
VD203 Algorithm for VLSI Design Automation	050	100	150	047	051	098
VD024 Designing With ASICs	050	100	150	047	070	117
PRACTICAL						
TOTAL						
	200	400	600	189	211	400

Total No.	Subject Sub	Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
				SECOND SEMESTER	Max. Marks	
1		VD201(4),	4	400	600	PASS

Checked by :

*[Signature]*

Printed on : 13/07/17

Controller of Examination

*[Signature]*

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 1248(2)XXIX-V-1-(ka)-19-2000 On 16th February, 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gautam Buddha Technical University, Lucknow and Mahatma Jyotiba Phule Technical University, Lucknow and a new University was created namely Mahatma Jyotiba Phule Technical University, Lucknow vide Notification No. 239(2)JL-XXIX-V-1-10-1-(ka)-6-2010. On 31st October, 2013 Gautam Buddha Technical University, Lucknow and Mahatma Jyotiba Phule Technical University, Lucknow were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324/16-1-2016-1(3)/2008. Government of UP, vide Notification No. 1156(2)JL-XXIX-V-1-19-1-(ka)24-2015 dated 9th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow