

DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

Master of Technology - FIRST YEAR STATEMENT OF MARKS

VLSI Design

Father's Name Name of Candidate

Name of Institution AVDHESH KUMAR TRIPATHI
DINESH CHANDRA TRIPATHI
SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY , ALLAHABAD

SECOND SEMESTER

SI. No.: 17961515

Session.: 2013-14

Roll No.: 1316208507 Enrollment No : 131620858249

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					Designing With ASICS	Algorithm for VLSI Design Automation	Low Power VLSI Design	Analog VLSI Design	A CONTRACTOR AND A CONT	
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400					100	100	100	100	-xam.	Maximum Marks:
600					150	150	150	150		rks:
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246	***************************************				066	067	057	056	Sess. Exam. Total	Obtained Marks:
429				no menta	112	112	102	103	Total	ks:

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Total No.

Carry Over (if any)

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SECOND SEMESTER

Marks Obtained / Maximum Marks

Max. Marks

Subject(s) Code

Printed on: 13/07/17

Controller of Examination

Note: The Uttar Pradesh Technical University, Lucknow was established by the Goverment of UP on 8th May, 2000 vide Act No. 1248(2)XVII-V-1-1(ka)-19-2000 On 16th February,2010, Uttar Pradesh Technical University, Lucknow was renamed as Gautam Buddh Technical University, use created namely Mahamaya Technical University, Noida, wide Notification No. 236(2)LXXIX-V-1-10-1(ka)-6-2010, On 31st October,2013 Gautam Buddh Technical University, Lucknow and Mahamaya Technical University, Noida were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324/16-1-2016-1(3)/2009, Government of UP, vide Notification No. 1156(2)/LXXIX-V-1-15-1(ka)/24-2015 dated 9th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J.Abdul Kalam Technical University, Uttar Pradesh, Lucknow