



# DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA  
**STATEMENT OF MARKS**

**Master of Technology - FIRST YEAR**

**VLSI Design**

Session.: 2014-15



Name of Candidate

: KUSHAGRA TRIPATHI

Father's Name

: VINOD KUMAR TRIPATHI

Name of Institution

: SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY, ALLAHABAD

Sl. No.: 17961533

Roll No.: 1316208517

Enrollment No : 131620858259

## SECOND SEMESTER

Subject Code and Name	Maximum Marks :			Obtained Marks :		
	Sess.	Exam.	Total	Sess.	Exam.	Total
VD201 Analog VLSI Design	050	100	150	046	040	086
VD202 Low Power VLSI Design	050	100	150	047	042	089
VD203 Algorithm for VLSI Design Automation	050	100	150	045	040	085
VD024 Designing With ASICS	050	100	150	046	053	099
PRACTICAL						
TOTAL						
	200	400	600	194	175	359

Total No.	Carry Over (if any)	Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
				SECOND SEMESTER	Max. Marks	
				359	600	PASS

Printed on : 14/07/17

Controller of Examination

Checked by: *[Signature]*

*[Signature]*  
Controller of Examination

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 1248(2)XVI-V-1-(6a)-19-2000 On 10th February 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gauran Budh Technical University, Lucknow and a new University was created namely Kashiya Technical University, Noida, vide Notification No. 236(2)XXIV-V-1-(6)-14-2010. On 31st October 2013 Gauran Budh Technical University, Lucknow and Kashiya Technical University, Noida were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324(VI-1-2014)-(3)2009. Government of UP, vide Notification No. 1159(2)XXIV-V-1-15-(VI)24-2015 dated 9th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow