



DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

STATEMENT OF MARKS

Master of Technology - FIRST YEAR

VLSI Design



1 5 1 6 2 0 8 5 1 4

Session: 2015-16

Name of Candidate : PRADUMN KUMAR

Father's Name : KEDAR NATH

Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING & TECHNOLOGY, ALHAHABAD

Sl. No.: 17961291

Roll No.: 1516208514

Enrollment No.: 151620808492

Subject Code and Name	Maximum Marks :		Obtained Marks :		
	Sess. Exam.	Total	Sess. Exam.	Total	
VD201 Analog VLSI Design	050	100	045	050	095
VD202 Low Power VLSI Design	050	100	046	049	095
VD203 Algorithm for VLSI Design Automation	050	100	045	044	089
VD024 Designing With ASICs	050	100	044	052	096
PRACTICAL					
TOTAL					
	200	400	180	195	375

Total No.	Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
			SECOND SEMESTER	Max. Marks	
			375	600	PASS
CARRY OVER (if any)					

Checked by:

Printed on: 16/08/17

Note: The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 129(2)XXIII/1-1189-19-2000 On 18th February, 2010, Uttar Pradesh Technical University, Lucknow was renamed as Galileo Baidh Technical University, Lucknow and a new University was created namely Mahatma Technical University, Noida, vide Notification No. 236(2)XXIX/1-1-10-16(1)-2010, On 31st October 2013 Galileo Baidh Technical University, Lucknow and Mahatma Technical University, Lucknow were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 332(4)B-1-2015-1(3)/2009, Government of UP, vide Notification No. 156(2)XXIX/1-15-16(1)24-2015 dated 8th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

Controller of Examination