



DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

STATEMENT OF MARKS

Master of Technology - FIRST YEAR

VLSI Design

Session: 2015-16

Name of Candidate
Father's Name
Name of Institution

: RAHUL KUMAR
: SATYANARAYAN
: SHAMBHUNATH INSTITUTE OF ENGINEERING & TECHNOLOGY, ALLAHABAD

Sl. No.: 17961292
Roll No.: 1516208515
Enrollment No.: 151620808493

SECOND SEMESTER

Subject Code and Name	Maximum Marks :		Obtained Marks :			
	Sess.	Exam. Total	Sess.	Exam. Total		
VD201 Analog VLSI Design	050	100	043	048	091	
VD202 Low Power VLSI Design	050	100	046	061	107	
VD203 Algorithm for VLSI Design Automation	050	100	046	056	102	
VD024 Designing With ASICs	050	100	043	076	119	
PRACTICAL						
TOTAL						
	200	400	600	178	241	419

Total No.	Carry Over (If any)	Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
				SECOND SEMESTER	Max. Marks	
				419	600	PASS

Checked by:

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2003 vide Act No. 1249/XXI/14-1-1991-19-2002 On 16th February, 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gauri Baidh Technical University, Lucknow and a new University was created namely Mahatma Jyoti Bapu Technical University, Noida vide Notification No. 236/2010/XXI/14-1-1991-19-2002 On 31st October, 2013 Gauri Baidh Technical University, Lucknow and Mahatma Jyoti Bapu Technical University, Lucknow were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 1325/15-1-2015-11/3/2008, Government of UP vide Notification No. 1158/2/XXI/14-1-1991-19-2002 dated 08th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

Printed on : 17/06/17

Controller of Examination

