



DR. A. P. J. ABDUL KALAM TECHNICAL UNIVERSITY, UP
LUCKNOW, UTTAR PRADESH, INDIA
STATEMENT OF MARKS



Master of Technology - FIRST YEAR
VLSI Design

Session.: 2016-17

Name of Candidate : HARSHIT SWAROOP
Father's Name : ANAND SWAROOP SRIVASTAVA
Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING & TECHNOLOGY, ALLAHABAD

Sl. No.: 17810020
Roll No.: 1516208508
Enrollment No.: 151620808486

SECOND SEMESTER

Subject Code and Name	Maximum Marks :			Obtained Marks :		
	Sess.	Exam.	Total	Sess.	Exam.	Total
VD201 Analog VLSI Design	050	100	150	048	046	094
VD202 Low Power VLSI Design	050	100	150	046	056	102
VD203 Algorithm for VLSI Design Automation	050	100	150	047	041	088
VD024 Designing With ASICS	050	100	150	047	056	103
PRACTICAL						
TOTAL						
	200	400	600	188	199	387

Total No.	Carry Over (if any) Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks SECOND SEMESTER	Max. Marks	RESULT
			387	600	PASS

Checked by :

Printed on : 12/05/22

Controller of Examination

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 1248(2)XVII-V-1-1(ka)-19-2000 On 16th February, 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gautam Buddha Technical University, Lucknow and a new University was created namely Mahamaya Technical University, Noida, vide Notification No. 238(2)LXXIX-V-1-10-1(ka)-6-2010. On 31st October, 2013 Gautam Buddha Technical University, Lucknow and Mahamaya Technical University, Noida were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324/16-1-2016-1(3)/2009. Government of UP, vide Notification No. 1156(2)LXXIX-V-1-15-1(ka)24-2015 dated 9th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow