



DR. A. P. J. ABDUL KALAM TECHNICAL UNIVERSITY, UP

LUCKNOW, UTTAR PRADESH, INDIA

GRADE SHEET



Master of Technology - FIRST YEAR

VLSI Design

Session.: 2017-18

Name of Candidate : NAVEEN MISHRA
 Father's Name : SATISH MISHRA
 Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING & TECHNOLOGY, ALLAHABAD

Sl. No.: 17828501
 Roll No.: 1516208512
 Enrollment No.: 151620808490

SECOND SEMESTER

Subject Code and Name		Maximum Marks :			Obtained Marks :		
		Sess.	Exam.	Total	Sess.	Exam.	Total
VD201	Analog VLSI Design	050	100	150	045	45*	090
VD202	Low Power VLSI Design	050	100	150	046	062	108
VD203	Algorithm for VLSI Design Automation	050	100	150	046	048	094
VD024	Designing With ASICS	050	100	150	047	055	102
PRACTICAL							
TOTAL		200	400	600	184	210	394

Total No.	Carry Over (if any) Subject(s) Code	Grace Marks (if any)	Marks Obtained / Maximum Marks SECOND SEMESTER	Max. Marks	RESULT
			394	600	PASS

Checked by .

Printed on : 13/05/22

Controller of Examination

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 1248(2)XVII-V-1-1(ka)-19-2000 On 16th February,2010, Uttar Pradesh Technical University, Lucknow was renamed as Gautam Buddh Technical University, Lucknow and a new University was created namely Mahamaya Technical University, Noida, vide Notification No. 236(2)LXXIX-V-1-10-1(ka)-6-2010. On 31st October,2013 Gautam Buddh Technical University, Lucknow and Mahamaya Technical University, Noida were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324/15-1-2016-1(3)/2009. Government of UP, vide Notification No. 1156(2)LXXIX-V-1-15-1(ka)24-2015 dated 9th September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J.Abdul Kalam Technical University, Uttar Pradesh, Lucknow