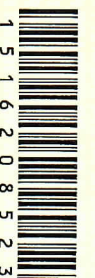# DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

STATEMENT OF MARKS

Master of Technology - FIRST YEAR

VLSI Design

Name of Candidate	:	VIRENDRA KUMAR YADAV
Father's Name	:	LAXMAN YADAV
Name of Institution	:	SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY, ALLAHABAD

Session.: 2015-16

Sl. No.: 17961501
Roll No.: 1516208523
Enrollment No.: 15162080501

FIRST SEMESTER

Subject Code and Name		Maximum Marks :			Obtained Marks :		
		Sess.	Exam.	Total	Sess.	Exam.	Total
VD101	Advance Computer Architecture	050	100	150	046	035	081
VD102	VIsi Technology And Design	050	100	150	045	040	085
VD103	Digital Design Using Vhdl	050	100	150	046	040	086
VD014	Solid State Electronic Devices	050	100	150	047	045	092
PRACTICAL							
TOTAL		200	400	600	184	160	344

Grace Sub		Grace Marks	Marks Obtained / Maximum Marks		
Total No.	Subject(s) Code	(if any)	FIRST SEMESTER		RESULT
			Max. Marks		
1	VD101(5),	5	344	600	PASS

Checked by : [signature]

Printed on : 13/07/17

Controller of Examination
[signature]

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