



DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

STATEMENT OF MARKS

Master of Technology - FIRST YEAR

VLSI Design

Session : 2015-16


Name of Candidate : CHAHAT VARSHNEY
Father's Name : RUPAK VARSHNEY
Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING & TECHNOLOGY, ALLAHABAD

FIRST SEMESTER

SI. No. : 17961282
Roll No. : 1516208506
Enrollment No : 151620808484

Subject Code and Name	Maximum Marks :			Obtained Marks :		
	Sess.	Exam.	Total	Sess.	Exam.	Total
VD101 Advance Computer Architecture	050	100	150	049	073	122
VD102 Vlsi Technology And Design	050	100	150	049	075	124
VD103 Digital Design Using Vhdl	050	100	150	049	052	101
VD014 Solid State Electronic Devices	050	100	150	049	059	108
PRACTICAL						
TOTAL						
	200	400	800	196	259	455

Total No.	Carry Over (if any)		Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
	Subject(s) Code			FIRST SEMESTER	Max. Marks	
			455	800	PASS	

Checked by : 

Note : The Uter Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 12&12XXXVI-VI-1-19&19-2000 on 18th February, 2010. Uter Pradesh Technical University, Lucknow was renamed as Gautam Budh Technical University, Lucknow and a new University was created named Mahatma J. Technol. University, Noida, vide Notification No. 23&21XXXVI-1-10-11&11-2010. On 31st October, 2013 Gautam Budh Technical University, Lucknow and Mahatma J. Technol. University, Noida were merged to form Uter Pradesh Technical University, Lucknow vide Notification No. 33&31&15-2013-1(3)2009, Government of UP, vide Notification No. 15&12&12XXXVI-1-15-11&11-2015 dated 9th September, 2015 renamed Uter Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uter Pradesh, Lucknow.

Printed on : 16/06/17

Controller of Examination 