

Father's Name Name of Candidate

SATYENDRA KUMAR
RAJBALI RAM
SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY , ALLAHABAD

Name of Institution

VD014 VD103 VD102 **VD101**

DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

Master of Technology - FIRST YEAR

VLSI Design

Session.: 2015-16

Roll No.: 1516208520 Enrollment No : 151620808498

Solid State Electronic Devices VIsi Technology And Design Digital Design Using Vhdl Advance Computer Architecture Subject Code and Name PRACTICAL TOTAL FIRST SEMESTER Sess. Exam. Total 050 050 200 Maximum Marks: 400 100 100 100 600 150 150 150 150 048 047 048 Sess. Exam. 190 047 Obtained Marks: 191 057 Total 381 089 087 100 105

PECHT	Marks Obtained / Maximum Marks	Warks Obtaine	Grace Marks	Carry Over (if any)	C
INFO OF I	Max. Marks	FIRST SEMESTER	(if any)	Subject(s) Code	
PASS	600	381			
RES	Max. Marks 600	FIRST SEMESTER 381	(if any)	Subject(s) Code	

Checked by : reals

Total No.

Checked by:

Printed on: 13/07/17

Controller of Examination

Note: The Utter Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 1248(2)XVII-X-1-1((ka)-19-2000 On 16th February,2010, Utter Pradesh Technical University, Lucknow was renamed as Gautam Buddh Technical University, Lucknow and Rechnical University, Noida were merged to form Utter Pradesh Technical University, Lucknow vide Notification No. 3324/16-1-2016-1(3)/2009. Government of UP, vide Notification No. 1156(2)/LXXIX-V-1-15-1(ka)/24-2015 dated 9th September, 2015 renamed Utter Pradesh Technical University, Lucknow as Dr. A.P.J.Abdul Kalam Technical University, Utter Pradesh, Lucknow Controller of Examination