



# DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY, U.P.

LUCKNOW, UTTAR PRADESH, INDIA

## STATEMENT OF MARKS

### Master of Technology - FIRST YEAR

#### VLSI Design



Session.: 2015-16

Name of Candidate : HARSHIT SWAROOP  
Father's Name : ANAND SWAROOP SRIVASTAVA  
Name of Institution : SHAMBHUNATH INSTITUTE OF ENGINEERING& TECHNOLOGY, ALLAHABAD

FIRST SEMESTER

Sl. No.: 17961285  
Roll No.: 1516208508  
Enrollment No.: 151620808486

Subject Code and Name	Maximum Marks :			Obtained Marks :		
	Sess.	Exam.	Total	Sess.	Exam.	Total
VD101 Advance Computer Architecture	050	100	150	048	040	088
VD102 Vlsi Technology And Design	050	100	150	047	040	087
VD103 Digital Design Using Vhdl	050	100	150	047	040	087
VD014 Solid State Electronic Devices	050	100	150	048	041	089
PRACTICAL						
TOTAL						
	200	400	600	190	161	351

Total No.	Carry Over (If any)		Grace Marks (if any)	Marks Obtained / Maximum Marks		RESULT
	Subject(s) Code			FIRST SEMESTER	Max. Marks	
			351	600	PASS	

Checked by:

Printed on : 16/06/17

Note : The Uttar Pradesh Technical University, Lucknow was established by the Government of UP on 8th May, 2000 vide Act No. 124 of 2000. On 16th February 2010, Uttar Pradesh Technical University, Lucknow was renamed as Gauri Baidh Technical University, Lucknow and a new University was created namely Kalamaya Technical University, Noida vide Modification No. 236 of 2010. On 31st October 2013 Gauri Baidh Technical University, Lucknow and Kalamaya Technical University, Lucknow were merged to form Uttar Pradesh Technical University, Lucknow vide Notification No. 3324/16-1-2014-1(3)/2009, Government of UP, vide Notification No. 1156/2013-15-11/124-2015 dated 21st September, 2015 renamed Uttar Pradesh Technical University, Lucknow as Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow

Controller of Examination